

# HP-SEE Using CUDA numerical libraries

[www.hp-see.eu](http://www.hp-see.eu)



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# HP-SEE

High-Performance Computing Infrastructure  
for South East Europe's Research Communities

# Agenda



**HP-SEE**

High-Performance Computing Infrastructure  
for South East Europe's Research Communities

- CUDA Showroom
- cuBLAS
- cuFFT
- cuRAND

# CUDA Showroom



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GPU Applications: <http://www.nvidia.com/object/gpu-applications.html>

GPU APPLICATIONS

Most Popular Search 1 of 1

<b>AMBER</b> <ul style="list-style-type: none"><li>89.44 ns/day JAC NVE Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>PMEMD: Explicit and Implicit Solvent</li></ul> <a href="#">Molecular Dynamics</a>	<b>GROMACS</b> <ul style="list-style-type: none"><li>165 ns/day DHFR Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>Implicit (5x)</li><li>Explicit(2x) Solvent</li></ul> <a href="#">Molecular Dynamics</a>	<b>LAMMPS</b> <ul style="list-style-type: none"><li>3.5 - 15x Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>Lennard-Jones</li><li>Gay-Berne</li></ul> <a href="#">Molecular Dynamics</a>	<b>NAMD</b> <ul style="list-style-type: none"><li>6.44 ns/day STMV585x 2050s Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>Non-Bond Force calculation</li><li>100M atom capable</li></ul> <a href="#">Molecular Dynamics</a>
<b>GeoVation CGG/Veritas RTM</b> <ul style="list-style-type: none"><li>Speed up non-disclosed</li></ul> Supported Features <ul style="list-style-type: none"><li>RTM algorithm</li></ul>	<b>Paradigm Echos RTM</b> <ul style="list-style-type: none"><li>14x Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>RTM algorithm</li></ul>	<b>Schlumberger WesternGeco Omega2 RTM</b> <ul style="list-style-type: none"><li>Non-disclosed</li></ul> Supported Features <ul style="list-style-type: none"><li>Multiple algorithms (RTM, etc)</li></ul>	<b>Tsunami RTM</b> <ul style="list-style-type: none"><li>5x Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>RTM algorithm</li></ul>
<b>NWChem</b> <ul style="list-style-type: none"><li>3-10x Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>Triples part of Reg-CCSD(T)</li><li>CCSD and EOMCCSD task schedulers</li><li>In development</li></ul> <a href="#">Computational Chemistry</a>	<b>ANSYS Mechanical</b> <ul style="list-style-type: none"><li>2-3x Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>Direct &amp; iterative solver</li></ul> <a href="#">Computational Structural Mechanics</a>	<b>Abaqus/Standard</b> <ul style="list-style-type: none"><li>1.5 - 2.5x Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>Direct sparse solver</li></ul> <a href="#">Computational Structural Mechanics</a>	<b>Impetus Afea</b> <ul style="list-style-type: none"><li>10x SPH, 2x total Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>linear equation solver</li></ul> <a href="#">Computational Structural Mechanics</a>
<b>MSC Nastran</b> <ul style="list-style-type: none"><li>1.5 Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>Linear equation solver</li></ul> <a href="#">Computational Structural Mechanics</a>	<b>MathWorks MATLAB</b> <ul style="list-style-type: none"><li>2-20x Speed up*</li></ul> Supported Features <ul style="list-style-type: none"><li>GPU acceleration for over 200 common MATLAB functions</li></ul> <a href="#">Computational Finance</a>	<b>VASP</b> <ul style="list-style-type: none"><li>2x Speed-up* 2 GPUs vs. 128 CPU cores</li></ul> Supported Features <ul style="list-style-type: none"><li>Hybrid Hartree-Fock DFT functionals including exact exchange</li></ul>	<b>Chroma</b> <ul style="list-style-type: none"><li>8-9x Speed up* 768 GPUs vs. 768 (16-core) CPUs</li></ul> Supported Features <ul style="list-style-type: none"><li>Wilson-clover fermions, Krylov solvers, Domain-decomposition</li></ul>

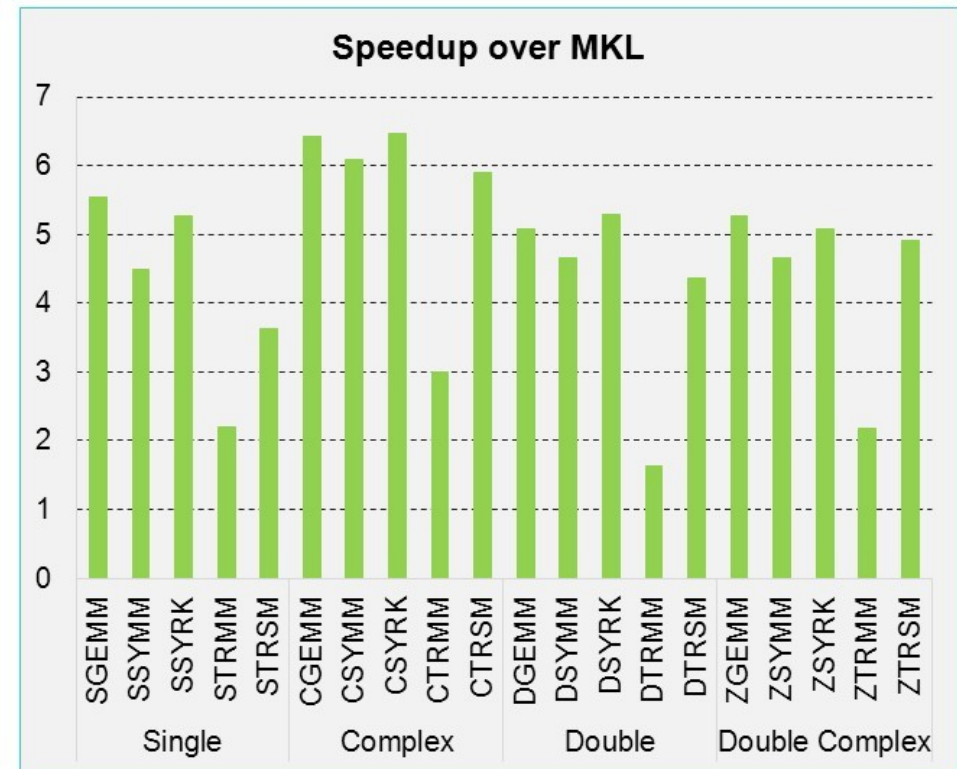
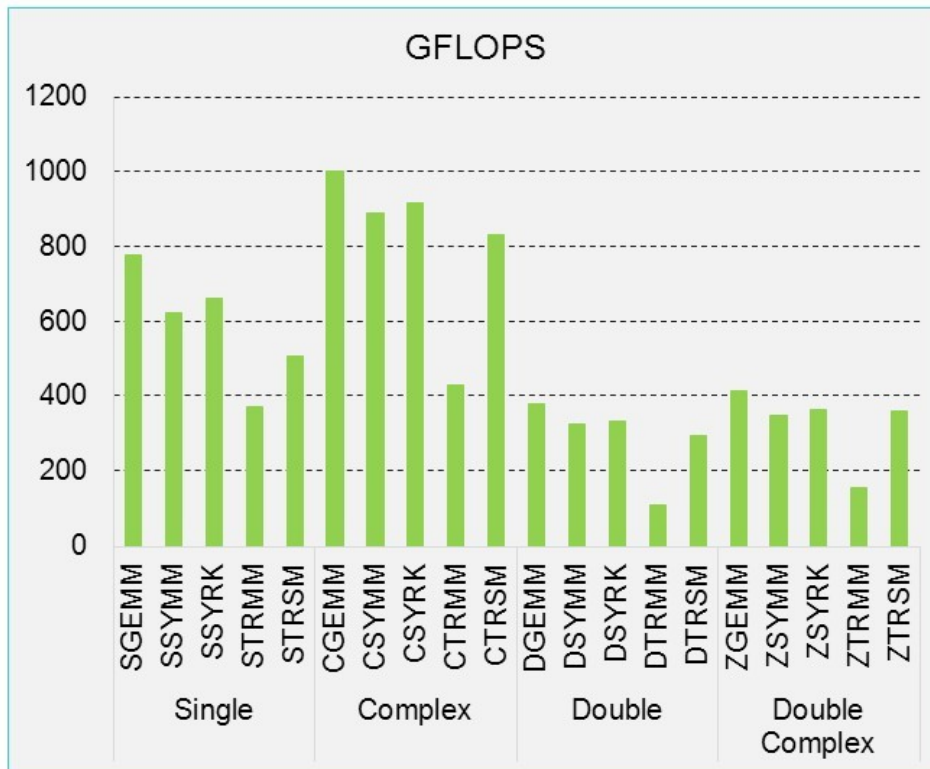
1 of 1



- Key features
  - Complete support for all 152 standard BLAS routines
  - Single, double, complex and double complex data types
  - Support for CUDA streams
  - Fortran bindings
  - Support for multiple GPUs and concurrent kernels
  - Batched GEMM API
  - Device API that can be called from CUDA kernels
  - Batched LU factorization API



- Performance

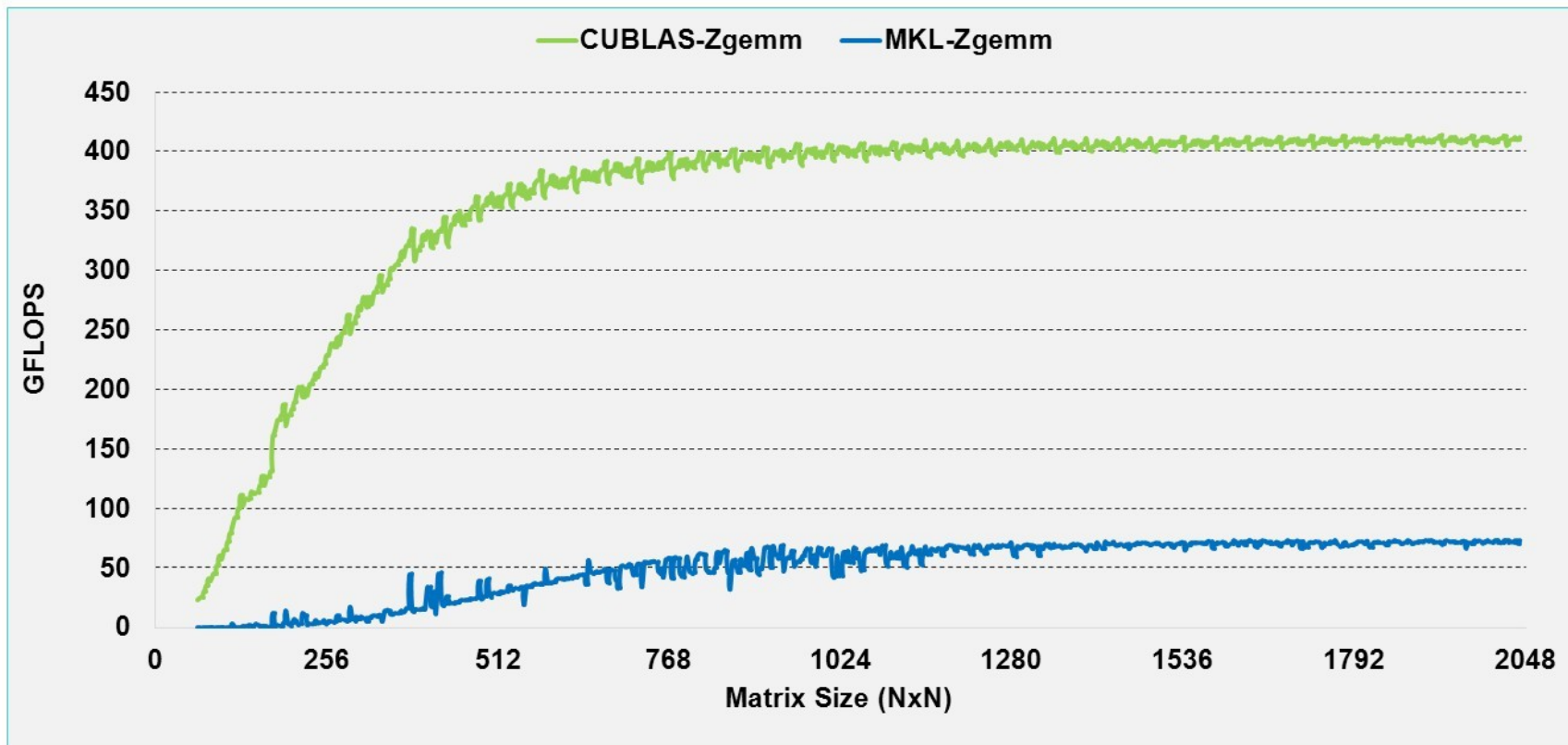


- 4Kx4K matrix size
- cuBLAS 4.1, Tesla M2090 (Fermi), ECC on

- MKL 10.2.3, TYAN FT72-B7015 Xeon x5680 Six-Core @ 3.33 GHz
- Performance may vary based on OS ver. and motherboard config



- Performance

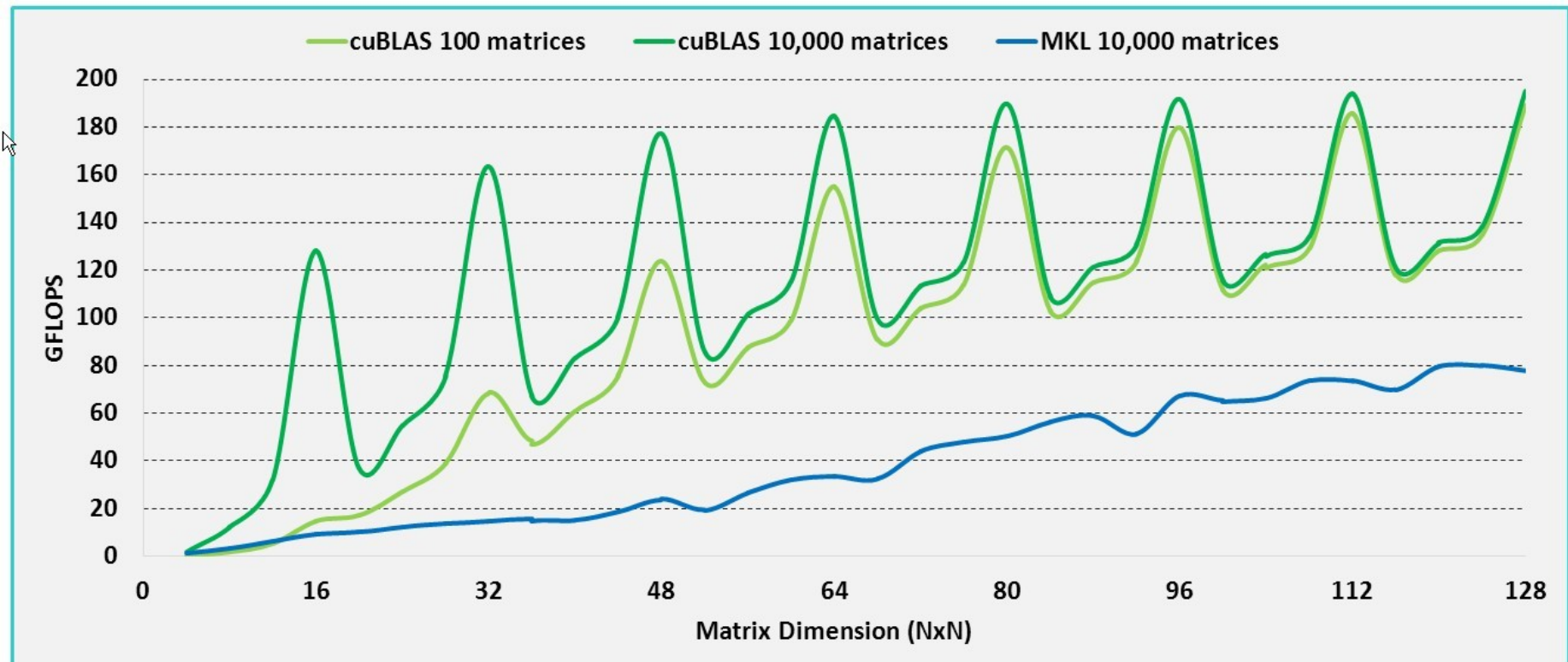


- cuBLAS 4.1 on Tesla M2090, ECC on
- MKL 10.2.3, TYAN FT72-B7015 Xeon x5680 Six-Core @ 3.33 GHz

• Performance may vary based on OS ver. and motherboard config.



- Performance



- cuBLAS 4.1 on Tesla M2090, ECC on
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• Performance may vary based on OS ver. and motherboard config.

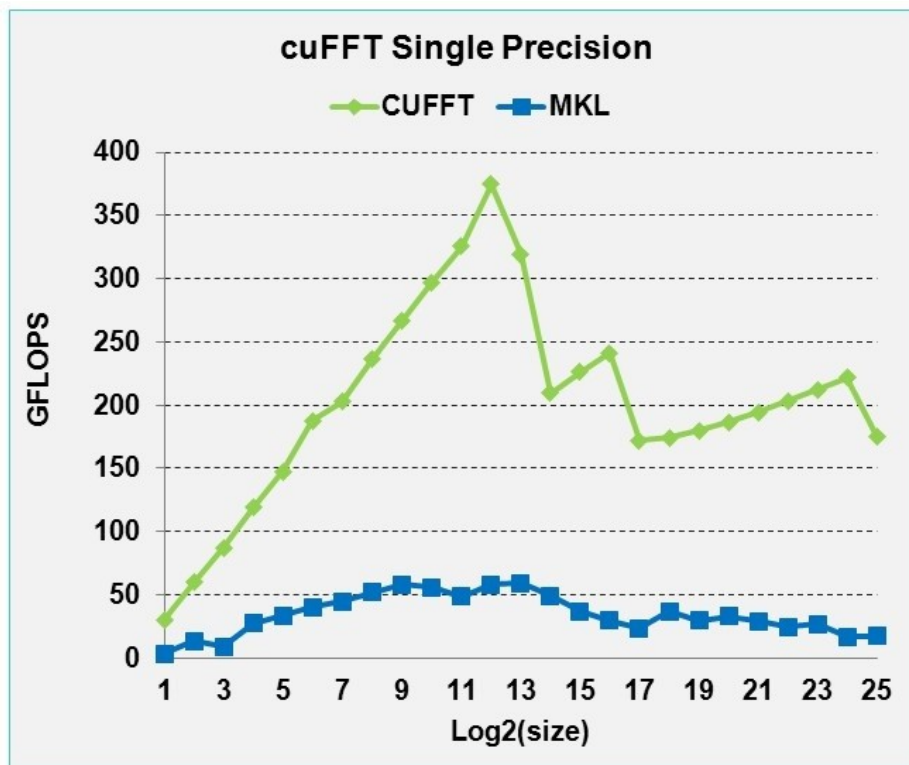


- Key features:
  - 1D, 2D, 3D transforms of complex and real data types
  - 1D transform sizes up to 128 million elements
  - Flexible data layouts by allowing arbitrary strides between individual elements and array dimensions
  - FFT algorithms based on Cooley-Tukey and Bluestein
  - Familiar API similar to FFTW Advanced Interface
  - Streamed asynchronous execution
  - Single and double precision transforms
  - Batch execution for doing multiple transforms
  - In-place and out-of-place transforms
  - Flexible input & output data layouts, similar to FFTW Advanced Interface
  - Thread-safe & callable from multiple host threads

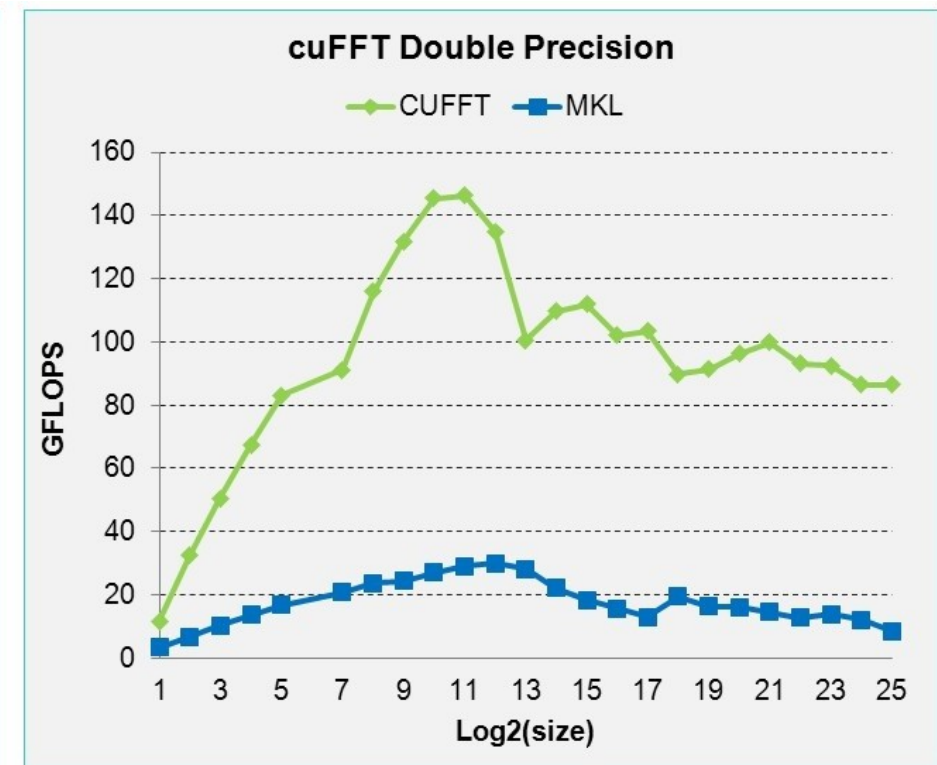




- Performance



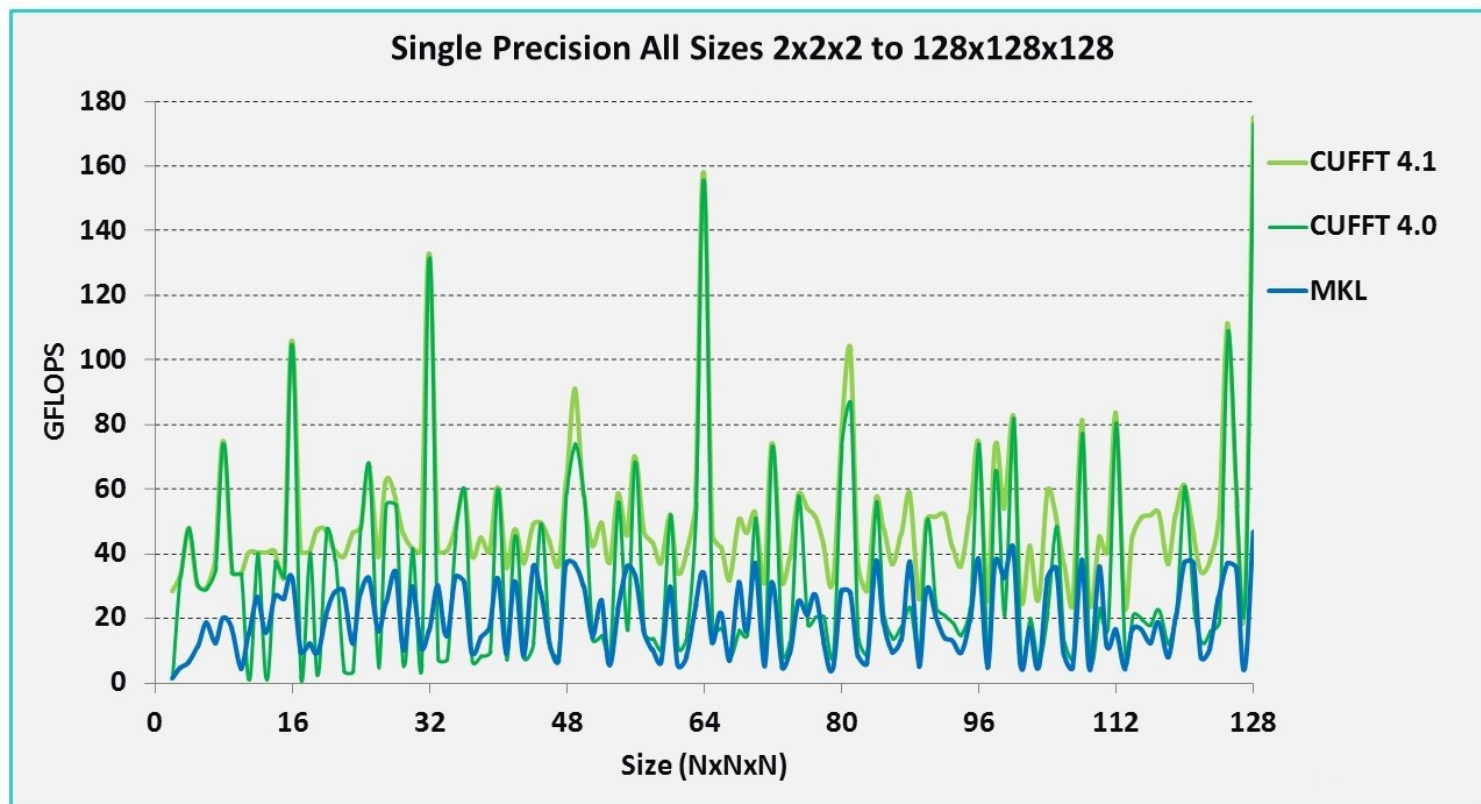
- Measured on sizes that are exactly powers-of-2
- cuFFT 4.1 on Tesla M2090, ECC on
- MKL 10.2.3, TYAN FT72-B7015 Xeon x5680 Six-Core @ 3.33 GHz



- MKL 10.2.3, TYAN FT72-B7015 Xeon x5680 Six-Core @ 3.33 GHz
- Performance may vary based on OS version and motherboard configuration



- Performance



- cuFFT 4.1 on Tesla M2090, ECC on
- MKL 10.2.3, TYAN FT72-B7015 Xeon x5680 Six-Core @ 3.33 GHz

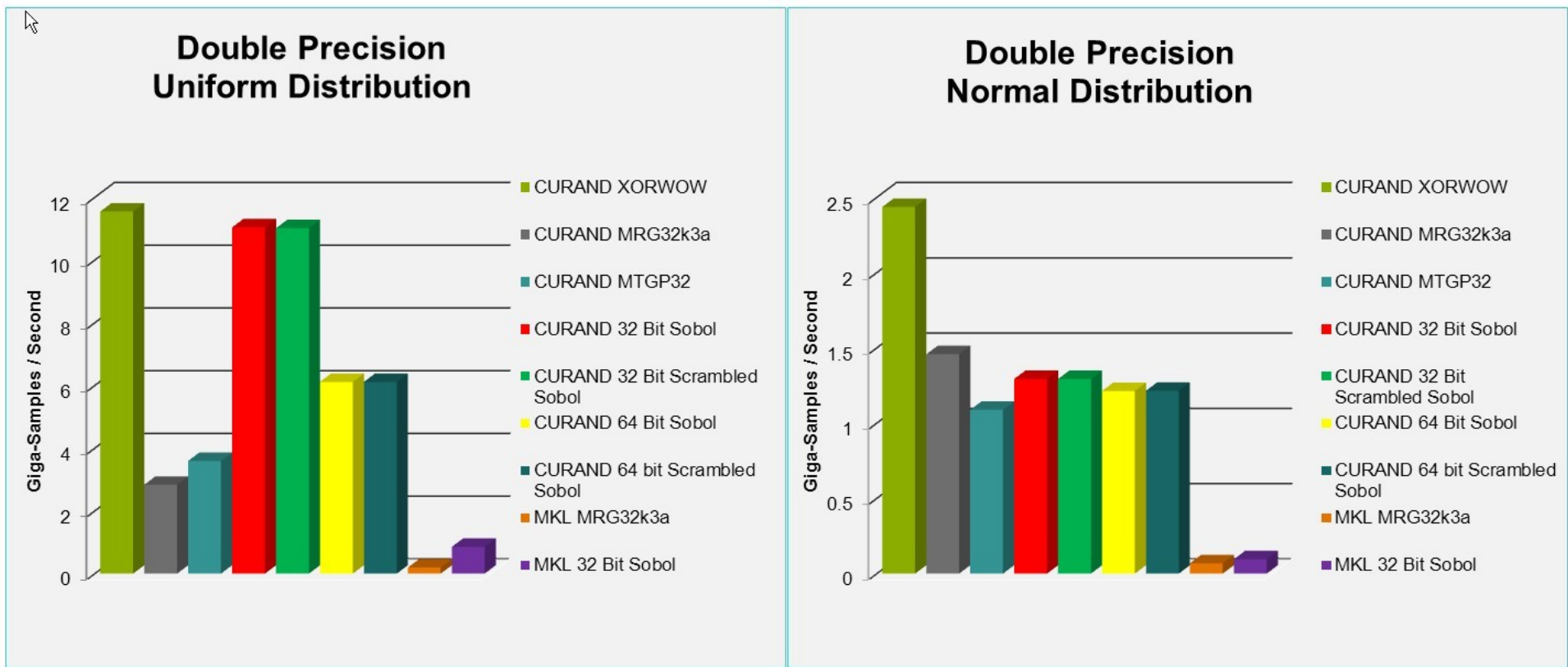
- Performance may vary based on OS ver. and motherboard config.



- Key features:
  - Flexible Usage Model
    - Host API for generating random numbers in bulk on GPU
    - Inline implementation allows use inside GPU functions/kernels, or in your host code
  - Four High-Quality RNG Algorithms
    - MRG32k3a
    - MTGP Merseinne Twister
    - XORWOW pseudo-random generation
    - Sobol' quasi-random number generators, including support for scrambled and 64-bit RNG
  - Multiple RNG Distribution Options
    - Uniform, normal, log-normal, Poisson distribution
    - Single or double precision



- Performance



- cuRAND 4.1 on Tesla M2090, ECC on
- MKL 10.2.3, TYAN FT72-B7015 Xeon x5680 Six-Core @ 3.33 GHz

• Performance may vary based on OS ver. and motherboard config.